1.	Depletion regions has						
	a. Electrons only	b. holes only c	. Neither Electrons nor Holes	s d. Both Electrons and Holes			
2.	For rectification we use	e					
	a. Diode b. Trar	sformer	c. capacitor	d. none of these			
3.	Reverse biasing the CE	junction of a trans	istor				
	a. Reverse -biases the EB junction b. Forward biases the EB junction						
	c. Is necessary for it to amplify d. Is not necessary for it to amplify						
4.	Specially designed semi conductor diodes used as indicator lamps in electronic circuits are						
	a. Photodiodes b. Solar cells c. LED d. Photovoltaic cell						
5.	Which one of the following is correct for a common emitter (CE) amplifier?						
	a. Its current gain β is high b. It has very high voltage gain c. It produces high power gain d. All of them						
6	Since the input resistance of an ideal OP-amplifier is infinite						
0.	a, its output resistance is zero		b. It becomes a current controlled device				
	c. its input current is zero		d. none of them				
7	An open loop OP-ampl	ifier has output vol	tage V_{0} and difference betwee	een non inverting input voltage Vi its open loop)		
7.	gain is given by						
	a $A_{OI} = V_0/V_1$	$h A_{OI} = V_i / V_o$	$c A_{OI} = V_0 x V_1$	d none of the two			
8	The closed loop gain of an OP-amplifier is $G = -R_2/R_1$ the negative sign shows that output signal is out of phase wr t						
	input signal hy						
	a 45°	b 90°	$c 180^{0}$	d 270°			
9	Linear integrated circuit	its (LICs) find their	applications in				
<i>)</i> .	a operational amplifiers h voltage comparators c comparator d all of them						
10	The emitter-base junction of a transistor is usually						
10.	a forward biased	b reverse biased	c non-conduction	d working in the break-down region			
11.	The output of 2-input Or gates is 0 only when its						
	a, both inputs are 0 b, either input is 1 c, both inputs are 1 d, either input is 0						
12.	An XOR gate produces	an output only wh	en its two inputs are				
12.	a high b low c different d same						
13.	Equivalent logic circuit of XOR gate can be designed by the combination of						
	a AND OR and NOT gates b NAND NOR and NOR gates						
	c. NOT, AND, and NOR gates d. NOT, AND and NOR gates						
14.	Equivalent logic circuit of XNOR gate can be designed by the combination of						
	a.AND, OR, and NOT gate b. AND, NOR, and NOT gate						
	c NAND AND and NOR gate d NAND AND and NOR gates						
15.	Donor impurities are	8		6			
	a. Germanium, Silicon etc b. Indium, Gallium etc c. Antimony arsenic etc d. Sodium. Zinc etc.						
16	In full wave rectification the output DC voltage across the load is obtained for						
	a. The positive half cvc	le of input AC only	b. The negative	ve half cycle of input AC only			
	c. The completes cycle	of input AC only	d. All of the a	above			
17.	Demodulation	I					
	a. Is performed at the t	ansmitting station	b. Removes side band	ls			
	c. Rectifies modulated	signal	d. Is opposite to of m	odulation			
18.	In amplitude modulation	n	The second se				
	a. Carrier frequency is	changed b	Carrier amplitude is change	ed			
	c. Three sides bands are produced d. Fidelity is improved						
19.	100% modulation is produced in AM when carrier						
	a. Frequency equals sig	anal frequency	b. Frequency exceed	signal frequency			
	c. Amplitude equals sig	anals amplitude	d. Amplitude exceed	signals amplitude			
20.	A non conducting semi	conductor diode is	F				
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	a. Forward biased b. Poorly biased	d c. Reverse biased d. N	None				
21.	Special algebra used in digital system is called a De-Morgan Algebra b Bernoulli's Algebra c Boolean's Algebra d All of the above						
22.	Photovoltaic cell is device that converts						
	a. Light energy into electrical energy b. Chemical energy into electrical energy						
	c. Light energy into sound energy d. None of these						
23.	Identify which one is might be the most importa	nt building block of Circuit any com	plex; electric circuit				
24	a. aloue U. Resistor C. Intermistor a. Ampliner						
24.	a. majority carriers b. minority carries c fixed rows of oppositely charged ions d depletion layer						
25.	The barrier potential of silicon diode at room ter	mperature is .	tons u. depiction hayer				
	a. 0.3 V b. 0.7 V	c. 1 V	d. 2mV				
26.	The width of depletion region of a junction						
	a. increases under forward bias	b. is independent of applied voltage					
27	c. increases under revere blas	d. None of them					
27.	a forward biased b poorly biased	c, reverse biased	d None				
28.	The characteristics of a transistor are	e. leverse blased	d. Hone				
	a. Light dependents b. Temperature dependents	ant c. Energy dependent	d. Sound dependant				
29.	A photo diode is usually made from		-				
•	a. bismuth b. antimony	c. Silicon	d. None				
30.	A light emitting diode (LED) is made from a callium arganida ($CaAa$) h callium above	nhida (CaP) a callium anania nhan	nhida (CaAaD) d all af tham				
31	philde (GaASP) d. all of them						
51.	a, forward biased b, reverse biased	c. unbiased d. none of them					
32.	A photo-diode can switch its current ON or OFF in						
	a. milli-seconds b. micro-seconds	c. nano-second d. c	centi-second				
33.	The term transistor stands for						
24	a. transfer resistor b. transfer of voltage	c. transfer of power d. t	ransfer of current				
34.	The emitter of a transistor is generally doped the heaviest because it a has to dissinate maximum power.						
	c. is the first region of the transistor d. must posses low resistance						
35.	For proper working of transistors in normal circuit						
	a. emitter-base junction is reverse biased and collector-base junction is forward biased						
	b. emitter-base junction is forward biased and collector-base junction is reverse biased						
	c. emitter-base junction is forward biased and collector-base junction is forward biased						
36	Transistor is an electronic device that has						
50.	a. two terminals b. Three terminals	c. four terminals d. 1	None of them				
37.	For common-emitter configuration of n-p-n tran	sistor, the current gain β is given by	У				
	a. I_C/I_B b. I_B/I_c c. I_E/I_B	$d_{\rm B}$ $d_{\rm E}/I_{\rm B}$	-				
38.	The transistor is basically a						
	<i>a.</i> Voltage amplifier b. current ampl	ifier c. Rectifier	d. None of these				
39.	A diode can convert	to as but not as into de					
	a. ac into de but not de into ac b. de into ac d. peith	le ac but not ac into ac					
40.	In a transistor	lei de into de noi de into de					
	a. Length of emitter is greater than that of collect	tor b. Length of collector is gre	ater is greater than that of emitter				
	c. 80th collector and emitter have the same leng	th d. Anyone of emitter or coll	lector can have greater length				
41.	In a transistor, the basic is a. An insulator b. A conductor of higher resistance c. A conductors of lowers resistance d. An extrinsic semiconductors						